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APPLICATION NO.	APPLICATION NO. FILING DATE FIRST NA		ED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/992.864	09/992.864 11/16/2001		L. Deeney	10015582-1	5995	
	METHOD AND APPAILA PORT SHIMS	TUS FOR SUPPORTING C	ircuit component hav	ing solder column a	RRAY INTERCONNECTS	
AI-PLN. TYPE	SMALL ENTITY	ISSUE PEE	PUBLICATION FEE	TOTAL FEB(S) DUE	DATE DUE	
nanprovisional	NO	\$1330	\$300	\$1630	U4/30/2004	
EXAMINER		ART UNIT	CLASS-SUBCLASS	7		
BUI, HUNG \$		2841	361-762000			
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RE:

PDNO

10015588-1

ATTY S/N

AWW 09/992,864

FILING DATE INVENTOR

11/16/2001 Jeffrey L. Deeney

TITLE

Method And Apparatus For Supporting Circuit Component Having Solder

Column Array Interconnects Using Interposed Support Shims

Please acknowledge receipt of the following documents for the above referenced Patent Application:

Fee(s) Transmittal - In Duplicate